

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR: BREISCH *et al.*

APPL. NO. To Be Assigned
(Continuation of USSN 10/060,363)

FILING DATE: September 16, 2003

TITLE: Dual Referenced Microstrip

GROUP ART UNIT: To Be Assigned

EXAMINER: To Be Assigned

Mail Stop Patent Application

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97(b)

Sir:

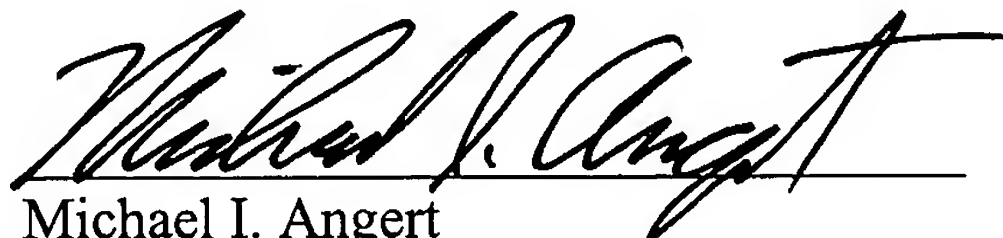
In conformance with Applicants' duty of disclosure under 37 C.F.R. § 1.56 and § 1.97, the references listed on the attached PTO-1449 (1 sheet) are hereby brought to the Examiner's attention.

In accordance with 37 C.F.R. § 1.98(d), copies of each reference are not provided as they were previously cited by or submitted to the Office in prior application Serial No. 10/060,363.

It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear in the "references cited" on any patent to issue therefrom.

Respectfully submitted,

Date: September 19, 2000


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